

Notice of Allowability

Application No.

10/775,250

Examiner

Eric W. Thomas

Applicant(s)

TOGASHI ET AL.

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the amendment filed on 5/16/04.
2. ☒ The allowed claim(s) is/are 1 and 4-33.
3. ☒ The drawings filed on 11 February 2004 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).


* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☒ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____


ERIC W. THOMAS
PRIMARY EXAMINER

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mark Woodall on 6/10/05.

The application has been amended as follows:

Rewrite claim 1:

1. (Currently Amended) A multilayer capacitor₁ comprising:
a dielectric layer and
two types of first and second internal conductor layers insulated from each other by said dielectric layer and alternately arranged in a dielectric body,
said multilayer capacitor characterized in that
the first internal conductor layer is formed with at least one first cut part,
the second internal conductor layer is formed with at least one second cut part,
due to said cut parts, each internal conductor layer is formed with at least two channel parts connected at an uncut end in the same plane and the channel parts adjoining each other in the same plane carry current flowing in the reverse directions,
the first conductor layer is formed with a first lead part and the second conductor layer is formed with a second lead part at a position different from said first lead part so that current flows in reverse directions between the channel parts formed at the first and

second internal conductor layers adjoining each other across the dielectric layer, and all of the [said] lead parts of said capacitor are formed so as to be led out to only one surface of said dielectric body.

Rewrite claim 31:

31. (Currently Amended) A multilayer capacitor, comprising:
a dielectric layer; and
two types of, that is, first and second, internal conductor layers insulated from each other by said dielectric layer and alternately arranged in a dielectric body each of said internal conductor layers having a lead part,

said multilayer capacitor characterized in that
the first internal conductor layer is formed with at least one first cut part,
the second internal conductor layer is formed with at least one second cut part,
due to said cut parts, each internal conductor layer is formed with at least two channel parts connected at an uncut end in the same plane and the channel parts adjoining each other in the same plane carry current flowing in the reverse directions, wherein

the first internal conductor layer is formed with a plurality of first cut parts,
the second internal conductor layer is formed with a plurality of second cut parts at positions corresponding to said first cut parts, and
uncut ends of corresponding cut parts across the dielectric layer are formed at opposite sides along the longitudinal direction of said cut parts, and all of the lead parts

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of said capacitor are formed so as to be led out to only one surface of said dielectric body.

2. The following is an examiner's statement of reasons for allowance: The prior art does not teach or fairly suggest (taken in combination with the other claimed features) a multilayer capacitor wherein all of the lead parts of said capacitor are formed so as to be led out to only one surface of said dielectric body (claims 1, 4-33).

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Eric W. Thomas whose telephone number is 571-272-1985. The examiner can normally be reached on Monday - Friday 5:30 AM - 2:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dean Reichard can be reached on 571-272-1984. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Handwritten signature of Eric W. Thomas and the date 6-10-05.

ERIC W. THOMAS
PRIMARY EXAMINER